

<b>Notice of References Cited</b>	Application/Control No. 10/616,021	Applicant(s)/Patent Under Reexamination LEE ET AL.	
	Examiner Linda Wong	Art Unit 2611	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
.	A	US-5,373,257	12-1994	Shimoda, Kaneyasu	331/8
	B	US-			
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	D	US-			
	E	US-			
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	I	US-			
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	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

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	N					
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#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Song et al, "A 4-Gb/s Clock and Data Recovery Circuit Using Four-Phase 1/8 Rate Clock", 24-26 Sept 2002, ESSCIRC2002 Proceedings of the 28th Solid-State Circuits Conference, pp 239-242
	V	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.